

# Contents

**Technical Reference Notice Of Disclaimer . . . . . iii**

## **1 Introduction**

1.1 Digital Cross-Connect System . . . . . 1-1  
1.2 Change History . . . . . 1-1  
1.3 Organization and Scope . . . . . 1-2  
1.4 Requirements Terminology and Labeling Conventions . . . . . 1-2  
    1.4.1 Definitions . . . . . 1-2  
    1.4.2 Numbering of Requirement and Related Objects . . . . . 1-3  
    1.4.3 Requirement, Conditional Requirement, and Objective Identification . . . . . 1-4  
1.5 Feature Listing . . . . . 1-4  
    1.5.1 Required Features . . . . . 1-4  
    1.5.2 Optional Features . . . . . 1-5  
1.6 System Functionality . . . . . 1-6  
    1.6.1 Test Access . . . . . 1-6  
    1.6.2 Alterable Memory . . . . . 1-6  
    1.6.3 Self-Monitoring Capability . . . . . 1-7  
1.7 System Applications . . . . . 1-7  
    1.7.1 General . . . . . 1-7  
    1.7.2 Hubbing . . . . . 1-7  
    1.7.3 Consolidation and Segregation . . . . . 1-8  
    1.7.4 Back-to-Back Channel Bank Replacement . . . . . 1-8  
    1.7.5 Multichannel Cross-connect . . . . . 1-8  
    1.7.6 Integrated Network Access . . . . . 1-8  
    1.7.7 Customer-Controlled Services . . . . . 1-9

## **2 Network Compatibility Criteria**

2.1 Introduction . . . . . 2-1  
2.2 DSX-1 Compatibility . . . . . 2-1  
2.3 Transmission Requirements . . . . . 2-2  
    2.3.1 Synchronization . . . . . 2-2  
        2.3.1.1 Timing Modes . . . . . 2-2  
        2.3.1.2 Internal Clock Requirements . . . . . 2-2  
        2.3.1.3 Synchronization Performance Monitoring Criteria . . . . . 2-2  
        2.3.1.4 DS1 Traffic Interface . . . . . 2-3  
    2.3.2 Jitter . . . . . 2-3  
    2.3.3 Transmission Path . . . . . 2-3  
    2.3.4 Framing . . . . . 2-5  
        2.3.4.1 DS1 Reframe Time . . . . . 2-5  
        2.3.4.2 Out-of-Frame Detection . . . . . 2-5  
        2.3.4.3 False Framing . . . . . 2-5  
2.4 Signal Format . . . . . 2-6  
    2.4.1 Frame Organization . . . . . 2-6

2.4.2 Superframe Format . . . . .	2-6
2.4.3 Extended Superframe Format . . . . .	2-6
2.4.4 Signaling Freezing . . . . .	2-7
2.4.5 Channel Numbering/Identification . . . . .	2-7
2.5 Maintenance . . . . .	2-8
2.5.1 Facility Performance Monitoring . . . . .	2-8
2.5.2 Carrier Group Alarm Operation . . . . .	2-9
2.5.3 AIS Processing . . . . .	2-10
2.5.4 AIS Generation . . . . .	2-11
2.5.5 DCS CGA Plan . . . . .	2-12
2.5.6 External Alarms . . . . .	2-13
2.6 Operations Interfaces . . . . .	2-14

### 3 Feature Descriptions

3.1 Required Features . . . . .	3-1
3.1.1 Cross-Connect . . . . .	3-1
3.1.2 Signaling Formats . . . . .	3-1
3.1.3 Signaling Fixing . . . . .	3-2
3.1.4 Test Access . . . . .	3-2
3.1.5 Frame Maintenance . . . . .	3-4
3.1.6 Facility Termination and Maintenance . . . . .	3-4
3.1.7 Network Restoration Capability . . . . .	3-5
3.2 Optional Features . . . . .	3-6
3.2.1 1.544 Mb/s DS1 Cross-Connection . . . . .	3-6
3.2.2 DS1C Interface . . . . .	3-6
3.2.3 DS3 Interface . . . . .	3-8
3.2.4 SONET Interface . . . . .	3-8
3.2.5 Digital Multipoint Bridging . . . . .	3-8
3.2.6 TR-08 Interfaces . . . . .	3-9
3.2.7 DDS Capabilities . . . . .	3-9
3.2.7.1 DDS Interface . . . . .	3-9
3.2.7.2 Subrate Digital Data Cross-Connect Capability . . . . .	3-9
3.2.7.3 Subrate Data Multiplexing Capability . . . . .	3-9
3.2.7.4 Multipoint Junction Unit Capability . . . . .	3-10
3.2.8 Zero Code Suppression Inhibit Capability . . . . .	3-10
3.2.9 Digital Equalization/Echo Control . . . . .	3-10
3.2.10 LBRV Cross-Connect . . . . .	3-10
3.2.11 Remote Modules . . . . .	3-11
3.2.12 Secondary Nonvolatile Memory Backup . . . . .	3-11
3.2.13 Integrated Test Head Function . . . . .	3-12

### 4 Design Criteria

4.1 Introduction . . . . .	4-1
4.2 Physical/Electrical Design Criteria . . . . .	4-1
4.2.1 Transmission Path Interfaces . . . . .	4-1
4.2.2 Architecture . . . . .	4-1

4.2.3 Growth . . . . .	4-2
4.2.4 Cross-Connect Network . . . . .	4-2
4.2.5 Blocking . . . . .	4-2
4.2.6 Cross-Connect Performance . . . . .	4-2
4.2.7 Transmission Delay . . . . .	4-3
4.2.8 Signaling Delay . . . . .	4-3
4.2.9 Signaling Distortion . . . . .	4-3
4.2.10 DS0 Error Performance . . . . .	4-4
4.2.11 Power . . . . .	4-4
4.2.12 Grounding . . . . .	4-4
4.2.13 Fault Tolerance . . . . .	4-4
4.2.14 Software Design . . . . .	4-5
4.3 Environmental Design Criteria . . . . .	4-5
4.3.1 Electrical Stress . . . . .	4-5
4.3.2 Physical Considerations . . . . .	4-5

## **5 Quality and Reliability Criteria**

### **Appendix A: References**

### **Appendix B: Acronyms**

### **Requirement-Object Index**

## List of Figures

Figure 1-1	DCS Functional Block Diagram . . . . .	1-10
Figure 1-2	DS0 Switching . . . . .	1-11
Figure 1-3	Hubbing . . . . .	1-12
Figure 1-4	Consolidation and Segregation . . . . .	1-13
Figure 1-5	Back-to-Back Channel Bank Replacement . . . . .	1-14
Figure 1-6	Multi-Channel Switching . . . . .	1-15
Figure 1-7	Integrated Network Access Concept . . . . .	1-16
Figure 2-1	Typical Central Office With DCS . . . . .	2-15
Figure 2-2	DS1 Frame Organization . . . . .	2-16
Figure 2-3	CFA Timing . . . . .	2-17
Figure 3-1	Cross-Connection . . . . .	3-12
Figure 3-2	Monitor Access . . . . .	3-13
Figure 3-3	Split Access . . . . .	3-13
Figure 3-4	Terminate and Leave Access Configuration . . . . .	3-14
Figure 3-5	DCS Local Test Access . . . . .	3-15
Figure 3-6	32-kb/s Cross-Connect – Translation . . . . .	3-15
Figure 3-7	Subrate Digital Data Cross-Connect . . . . .	3-16
Figure 3-8	DCS Remote Modules . . . . .	3-17
Figure 3-9	Protection Switching . . . . .	3-17

## List of Tables

Table 2-1	Channel Number/Time Slot Assignments . . . . .	2-7
Table 2-2	CGA TC Signal Definitions . . . . .	2-13
Table 2-3	CGA Modes . . . . .	2-13